Filing Date November 19, 2003

Title: METHOD AND SYSTEM FOR COALESCING INPUT OUTPUT ACCESSES TO A VIRTUAL DEVICE

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REMARKS

Applicants have carefully reviewed and considered the Restriction Requirement mailed on November 15, 2006, and the references cited therewith. Applicants elect, without traverse, Group I which includes claims 1-9, 18-22, and 26-30. Applicants respectfully cancel remaining claims 31-36 (Group II) without prejudice, and reserve the right to reintroduce them in a divisional application at a later date.

Affirmation of Election

Restriction to one of the following claims is required under 35 U.S.C. 121:

- 1. Claims 1-9, 18-22 and 26-30, drawn to executing a plurality of input output (I0) instructions from an intrusion stream during a signal virtualization event, classified in class 718, subclass 1.
- 2. Claims 31-36, drawn to executing a plurality of input output (I0) instructions from an intrusion stream during a signal virtual machine (VM) exit, in class 718, subclass 100.

RESPONSE TO RESTRICTION REQUIREMENT

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Conclusion

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Applicants respectfully submit that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicants' attorney (217-377-2500) to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 50-1624.

Respectfully submitted,

Customer No.: 45512 Tel: 217-377-2500

Date: December 4, 2006

Reg. No. 39,942

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 4th day of December, 2006.